IN THE CLAIMS:

Kindly replace the claims of record with the following full set of claims:

1. (Currently amended)   A chip for processing a content, comprising at least a microprocessor, characterized in that said chip includes an integrated non-volatile programmable memory of restoring protection data and protected data, said protection data being intended to ~~be used~~ provide a level for authorizing/denying access to said protected data by said microprocessor while a program is executed, wherein said level of protection data represents at least one of a software and a hardware protection scheme and is only modifiable so as to increase the protection.

2. (Cancelled)

3. (Previously presented)   A chip according to Claim 1, wherein said protection data includes a password, said access being authorized/denied through a password check.

4. (Previously presented)   A chip according to Claim 1, wherein said protected data include data to activate/deactivate an optional feature of the chip.

5. (Original)   A chip according to Claim 4, wherein said optional feature is a connection to an external device for downloading a program and/or data for said external device.

3

6. (Original) A chip according to Claim 4, wherein said protected data include data to activate/deactivate an external boot program for said microprocessor, said external boot program including instructions for downloading a new boot program for said microprocessor from an external memory.

7. (Previously presented) A chip according to Claim 1, wherein said protection data include a value defining an address limit from which from the data stored at said memory are protected data and access to such protected data is denied.

8. (Original) A chip according to Claim 7, wherein said protected data include programs and data operating a conditional-access dedicated microprocessor.

9. (Currently amended) A device intended to recover a content from a media and to process said content, said device including a connection to said media and a chip comprising:

an integrated non-volatile programmable memory of restoring protection data and protected data, said protection data being intended to represent a level for authorizing/denying access to said protected data by said microprocessor while a program is executed, wherein said level of protection data represents at least one of a software and a hardware protection scheme and is only modifiable so as to increase the protection as claimed in Claim 1.

10. (Original) A device as claimed in Claim 9, intended to process encrypted video/audio data.

11. (Currently amended) A method for obtaining a protected chip including at least a microprocessor, said method using a chip ~~as claimed in Claim 1~~, said method including ~~the steps of~~:

    using at least an authorized access to modify protected data in <u>an integrated</u> ~~said~~ non-volatile memory,

    protecting the access to said protected data in non-volatile memory by modifying protection data in order to deny said access<u>, wherein said protection data being intended to provide a level for authorizing/denying access to said protected data by said microprocessor while a program is executed, wherein said level of protection data represents at least one of a software and a hardware protection scheme and is only modifiable so as to increase the protection.</u>